

Amendments to the Specification:

Please replace the paragraph beginning on page 14, line 1, with the following rewritten paragraph:

Here, the current mirror circuit 35 according to the second embodiment is constituted to connect two output side FETs 24, 30 in two series with the input side ~~FET 3~~ FET 23 and two of output currents (mirror currents) I2, I3 correspond to an input current I1.

Please replace the paragraph beginning on page 14, line 13, with the following rewritten paragraph:

The capacitor C3 is interposed on a connecting path between a connecting portion of the diode D4 on a side of the output portion and the ground. The capacitor ~~C2~~ C4 is interposed between a connecting portion of the diode D5 on a side of an output portion and a power source voltage input portion 41. Further, the capacitor ~~C2~~ C4 is omissible depending on use or the like of the charge pump circuit.

Please replace the paragraph beginning on page 18, line 7, with the following rewritten paragraph:

Further, FET 24 is going to make current of an amount of the output current I2 in accordance with a mirror ratio relative to the input current I1 flow and therefore, the output current I3 is stabilized in accordance with stability of the input current I1, thereby, also voltage between the gate and the source of ~~FET 7~~ FET 27 is also stabilized. Therefore, FET 27 is going to make constant current flow in accordance with the stability of the voltage between the gate and the source and therefore, even when current flowing at FET 27 is assumedly going to increase instantaneously, a change to increase the current is restrained by

FET 27. Thereby, current flowing along the path P4 (current in discharging the capacitor C3) is stabilized by operation of FET 27.